(Mohd. Amir Ansari)
Assistant Professor, Department of ECE, Faculty of Engineering & IT,
Integral University, Lucknow
(8808499511, mamir@iul.ac.in)
(Google Scholar Citation |Orcid Id |Scopus |WOS |Research gate |linked in)

PROFILE:

- **Assistant Professor** in Electronics & Communication Engineering Department at Integral University Lucknow since 19-09-2012 to till date.
- **Assistant Professor** in Electronics & Communication Engineering Department at Dr. M.C. Saxena group of colleges, Lucknow, U.P. since 23-07-2012 to 18-09-2012.
- **Assistant Professor** in Electronics & Communication Engineering Department at G. L. Bajaj group of institution, Mathura, U.P. since 16-08-2011 to 22-07-2012.

RESEARCH INTEREST:

- Analog circuit design for low-power and high-performance applications in Analog mixed signal systems.
- Exploration of design automation tools and methodologies for efficient Analog mixed signal ICs.
- Nanoscale interconnects and their impact on performance and reliability in highly integrated circuits.
- Development of carbon and silicon-based nanomaterials for next-generation electronic devices.

SUMMARY OF RESEARCH ACCOMPLISHMENT:

- 2 SCI Research papers
- 3 Scopus Research Papers
- 2 Book chapters

PROFESSIONAL MEMBERSHIP:

- Life Member of Indian Society for Technical Education (ISTE), India. Membership number: LM 91013

COURSE TAUGHT:

- **UG Level:** Basic Electronics
  Computer Organization
  Digital Electronics
  Integrated Circuits

Data Communication and Computer Network
VLSI Design
Nanoelectronics

- **PG Level:** Semiconductor Device Modelling & Circuit Simulation
  VLSI Devices and Circuits

ADMINISTRATIVE/DEPARTMENTAL RESPONSIBILTY:

- Working as a DQAC Criteria II Member in the department from 2023 to till date.
- Worked as a DQAC Criteria I in charge in the department from 2022 to 2023.
- Worked as a DQAC Member secretary in the department from 2016 to 2022.
- Working as a Course Coordinator of UG Program till date.
- Working as a Convener of IPR Related Event of IQAC in the department since 2023 to till date.
- Working as a Member of Accreditation & Ranking Committee (ARC) in the department since 2023.
- Working as a Member of Disciplinary/ Anti Ragging Committee in the department since 2023.
- Worked as a Member of the FDP conducting committee for the year 2021.
- Worked as Technical Programme Co-Chair "First IEEE International Conference on Computational and Characterization Techniques in Engineering & Sciences" (CCTES-18) (IEEE Conference #44023) scheduled on September 14-15, 2018
- Worked as Finance Officer in International Symposium on "Computational and Characterization Techniques in Engineering & Sciences" (CCTES-17) held on March 20, 2017.
- Worked as Programme Coordinator in International Symposium on Computational and Characterization Techniques in Engineering and Sciences (CCTES) held on March 20, 2017.
- Worked as Co-Organizing Secretary in International Seminar on "Present Scenario & Future Prospectives of Research in Engineering and Sciences" (ISPSFPRES-17) held on January 21, 2017.
- Worked as Organizing Committee Member in National Seminar on "Power of Setting Goals" on August 20, 2016.

STUDENTS SUPERVISION:

- M.TECH DISSERTATION SUPERVISION
- Performance Evaluation of OP-amp with High supply Rejection in 180 nm CMOS Technology. (Akhtar Saleem Ansari, Integral University, Lucknow, May-2022)
- High Gain Miller Compensated Op-Amp with High supply rejection in 180 nm CMOS Technology. (Avishisht Kumar, Integral University, Lucknow, August-2020)
- Study and Performance Analysis of Turbo Decoder (Ekta Rai, Integral University, Lucknow, 2018)
- Analysis and Simulation of Two-Dimensional DG MOSFET (Awill Anurag Mishra, Integral University, Lucknow, October-2015)
- Design of Image Editing Portal Using MATLAB (Harshit Dua, Integral University, Lucknow, 2013)
- Text Extraction from Live Captured Image (Anshuman Prakash Singh, Integral University, Lucknow, 2013)

PUBLISHED/ACCEPTED SCI/SCOPUS RESEARCH PAPERS:

- **Ansari, M.A.,** Saeed, S.H., Balodi, D. (2022). Charge Pump-Phase Frequency Detector based Phase-Locked Loop for Modern Wireless Communication—A Review. In proceedings of Trends in Electronics and Health Informatics. Lecture Notes in Networks and Systems, vol 376. Springer, Singapore. https://doi.org/10.1007/978-981-16-8826-3_42
- **Ansari, M.A.**, Saeed, S.H., Balodi, D. Khan, M.N. (2024) A low leakage and high-speed phase frequency detector-charge pump designed in nano dimension-based CMOS technology. International Journal of Nano Dimension, 15(4). https://doi.org/10.57647/j.ijnd.2024.1504.29
- **Ansari, M.A**., Saeed, S.H., Balodi, D. Khan, I.U., Khan, Z.H. (2024). Design and Analysis of Low Jitter Charge Pump Phase Locked Loop Architecture and Loop Filter in CMOS Process in Journal of Electrical Systems, 20(10), (pp. 1713-1720). https://doi.org/10.52783/jes.5397

PAPER PUBLISHED IN INTERNATIONAL CONFERENCES:

- **Ansari, M.A.**, Saeed, S.H., Balodi, D. (2022). Charge Pump-Phase Frequency Detector based Phase-Locked Loop for Modern Wireless Communication—A Review. In proceedings of Trends in Electronics and Health Informatics. Lecture Notes in Networks and Systems, vol 376. Springer, Singapore. https://doi.org/10.1007/978-981-16-8826-3_42
- Ansari, A.S., **Ansari, M.A**., & Khan, I. (2021). Performance Evaluation of Operational Amplifier with High PSRR in 0.18 µm CMOS Technology. 2021 10th International Conference on System Modeling & Advancement in Research Trends (SMART), 618-621. doi**:** 10.1109/SMART52563.2021.9676329

PUBLISHED NON-SCI-SCOPUS BUT PEER REVIEWED RESEARCH PAPERS:

- Avishisht Kumar, **Mohd. Amir Ansari**, Piyush Charan, "Two Stage High Gain Op- Amp design with a High Supply Rejection of -94.66 dB in 180-nm CMOS Process using Miller Compensation." in International Journal of Advanced Research in Electronics and Communication Engineering. Vol 9, Issue 6, 21 June 2020, pp: 27-32.
- Ekta Rai, **Mohd. Amir Ansari,** Mohd Javed Khan- "A review on the performance of Turbo Encoder/Decoder" International Journal of Science and Advance Research in Technology (IJSART), Volume 4, Issue 10 October 2018 Edition. ISSN: 2395-1052
- Piyush Charan**, Mohd Amir Ansari,** Zohaib Hasan Khan, Mohd Tabrez Alam,"*Safe Multicast approach in Wireless MAN"*, International Journal for Scientific Research & Development, Vol. 3, Issue 07, ISSN (online): 2321-0613, August-2015.
- Awill Anurag Mishra, **Mohd. Amir Ansari,** Ahmad Shadab, Mohd. Khursheed Siddiqui, *"CMOS Implementation of Cascaded Instrumentation Amplifier with DC Offset voltage Reduction"* International Journal of Enhanced Research in Science Technology and Engineering, ISSN: 2319-7463, Vol.4, Issue5, May-2015, pp: 8-17
- **Mohd. Amir Ansari,** Awill Anurag Mishra, Ahmad Shadab, Mohd. Khursheed Siddiqui, *"Fate of MOSFET anatomy implementing double gate to dwindle the short channel aspects and drain induced barrier lowering"* International Journal of Enhanced Research in Science Technology and Engineering,

ISSN: 2319- 7463, Vol.4, Issue4, April-2015, PP: 125-131.

- Zohaib Hasan Khan, Piyush Charan, **Mohd Amir Ansari,** Kashif Ul Hasan Khan, "*Cybersquatting and its Effectual Position in India"* International Journal of Scientific & Engineering Research, Volume 6, Issue 2, February-2015, ISSN 2229- 5518, pp: 880-886.
- **Mohd. Amir Ansari,** Harshit Dua, Anshuman Prakash Singh, Vaibhav Siddharth**, "***Contrast of Non-Linear Interpolation Techniques for Image Remapping*" International Journal of Engineering and Technical Research (IJETR), ISSN: 2321- 0869, Volume-2, Issue-3, March 2014.
- Nupur Mittal, Imran Ullah Khan, Firdaus Majeed**, Mohd. Amir Ansari, "***Performance Evaluation of the Second Generation Current Controlled Conveyor (CCCII) and Analysis of Simple Voltage and Current Amplifier Based on it"* International Journal of Technical Research and Application (IJTRA), e- ISSN: 2320-8163, Volume-2, Issue-6 (Nov-Dec 2014), pp: 107-110
- **Mohd. Amir Ansari,** Mohammad Arshad, – "*Performance Analysis of Dispersed Manage RZ Pulse*"- published at: "International Journal of Scientific and Research Publications (IJSRP), Volume 3, Issue 12, December 2013 Edition. ISSN 2250-3153.

BOOK CHAPTERS:

- Book Chapter titled (2022). Charge Pump-Phase Frequency Detector based Phase-Locked Loop for Modern Wireless Communication—A Review. In: Kaiser, M.S., Bandyopadhyay, A., Ray, K., Singh, R., Nagar, V. (eds) Proceedings of Trends in Electronics and Health Informatics. Lecture Notes in Networks and Systems, vol 376. Springer, Singapore. Online ISBN 978-981-16-8826-3 https://doi.org/10.1007/978-981-16-8826-3_42

- Book Chapter titled (2023). Applications of VLSI Design in Artificial Intelligence and Machine Learning. In Machine Learning for VLSI Chip Design (eds A. Kumar, S.L. Tripathi and K. Srinivasa Rao). https://doi.org/10.1002/9781119910497.ch1
